

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Andrew G. Haerle, et al.

Title: METHOD FOR TREATING SEMICONDUCTOR PROCESSING
COMPONENTS AND COMPONENTS FORMED THEREBY

Application No.: NEW APPLICATION Filed: HEREWITH

Atty. Docket No.: 1075-E4283-DIV

MS PATENT APPLICATION
COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, VA 22313-1450**INFORMATION DISCLOSURE STATEMENT TRANSMITTAL**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
☐ Other: n/a

to the Examiner's attention in the above-identified application. These references were cited in parent Application No. 10/414,563, filed April 15, 2003. Accordingly, in accordance with C.F.R. § 1.98(d), copies of the references are not being supplied herewith. Citation of such information shall not be construed as:

1. an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described below; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance, such as through (i) an English language abstract, (ii) an English language equivalent application, (iii) reference to discussion in the application, or (iv) if cited in a search report or other action by a foreign patent office in a

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as either First Class Mail or Express Mail, in an envelope addressed to the Commissioner for Patents on 4-21-04.

Elise K. Dougherty
Typed or Printed Name

E.K. Dougherty
Signature

counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

STATEMENT UNDER 37 C.F.R. § 1.704(d)

If the above-identified application is an original application filed on or after
May 29, 2000:

- ☐ each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.

FEES DUE


This Information Disclosure Statement is being filed:

- ☒ within three months of the filing date of a national application or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☐ before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.
- ☐ during the period specified in § 1.97(c). Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application not more than three months prior to the filing of this Information Disclosure Statement.
- ☐ during the period specified in § 1.97(c). Accordingly, the fee set forth in § 1.17(p) is required and provided as shown on the attached Fee Transmittal.
- ☐ during the period specified in § 1.97(d). Accordingly, the fee set forth in § 1.17(p) is required and provided as shown on the attached Fee Transmittal. Additionally, each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application not more than three months prior to the filing of this Information Disclosure Statement.

Should any PTO fees be necessary for entry of this Information Disclosure Statement, the undersigned hereby authorizes the Commissioner to charge Deposit Account 50-2469.

4/21/04
Date

Respectfully submitted,


Jeffrey S. Abel, Reg. No. 36,079
Attorney for Applicant(s)
TOLER, LARSON & ABEL, L.L.P.
5000 Plaza On The Lake, Suite 265
Austin, Texas 78746
(512) 327-5515 (phone)
(512) 327-5452 (fax)

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Substitute for form 1449A/PTO

Sheet 1 of 1
(use as many sheets as necessary)
Complete if Known

Application Number: NEW APPLICATION
Filing Date: HEREWITH
First Named Inventor: Andrew G. Haerle
Group Art Unit:
Examiner Name:
Attorney Docket Number: 1075-E4283-DIV

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No.1	U.S. Patent Document Number	Kind Code 2 (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	6,093,644		Inaba et al.	07/25/2000	
	AB	6,277,194	B	Thilderkvist et al.	08/21/2001	
	AC	6,066,572		Lu, et al.	05/23/2000	
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No.1	Foreign Patent Document Office 3	Number	Kind Code 2 (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T 6
	AK							<input type="checkbox"/>
	AL							<input type="checkbox"/>
	AM							<input type="checkbox"/>
	AN							<input type="checkbox"/>
	AO							<input type="checkbox"/>

PUBLICATIONS

Examiner Initials *	Cite No.1	Title of Publication	Date of Publication of Cited Document MM-DD-YYYY
	AW	Wolf, et al., "Silicon Processing for the VLSI ERA, Volume I", PROCESS TECHNOLOGY, 1986, Lattice Press, pages 213-216.	
	AX	Ghandi, Sorab K., VLSI Fabrication Principles, Silicon and Gallium Aresnide, 1983, John Wiley and Sons, pages 517-520.	
	AY		
	AZ		

Examiner Signature	Date Considered
--------------------	-----------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Unique citation designation number. 2 See attached Kinds of U.S. Patent Documents. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

SEND TO: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450